

# PT32Y/X003 Series

## SELECTION GUIDE



Deep sleep mode current=3μA typ.

Part Number	Memory			Core	CPU Frequency MHz	Operating Voltage VDD (V)	GPIO I/O	Timer & Counter				Communication Interface			Clock			Analog		Security CRC	Operating Temperature °C	SWD Pins	Package Information			
	Flash (K)	RAM (K)	EEP (B)					Adv	Base	Low Power	WDG	UART	I2C	SPI	HSE (MHz)	HSI (MHz)	LSI (KHz)	ADC 12-bit	PVD				Package Types	Thickness	Pin Pitch	Packing
PT32Y003F4P6A	16	2	128	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	4~25	48	32	12ext + 2int	√	√	-40~85	8, 18	TSSOP20	1.2mm	0.65mm	Tube
PT32Y003F4P6B	16	2	128	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	4~25	48	32	12ext + 2int	√	√	-40~85	17, 18	TSSOP20	1.2mm	0.65mm	Tube
PT32Y003F4X6D	16	2	128	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	4~25	48	32	12ext + 2int	√	√	-40~85	5, 15	QFN20(3*3)	0.55mm	0.4mm	Tray
PT32Y003F4X6DR	16	2	128	M0	48	1.8-5.5	17+1	1	2	1	1	2	1	1	4~25	48	32	12ext + 2int	√	√	-40~85	5, 15	QFN20(3*3)	0.55mm	0.4mm	Tape & Reel
PT32X003F4P6A	16	2	480	M0	48	2.2-3.6	17+1	1	2	1	1	2	1	1	-	48	32	12ext + 2int	-	√	-40~85	8, 18	TSSOP20	1.2mm	0.65mm	Tube
PT32X003F4P6B*	16	2	480	M0	48	2.2-3.6	17+1	1	2	1	1	2	1	1	-	48	32	12ext + 2int	-	√	-40~85	17, 18	TSSOP20	1.2mm	0.65mm	Tube
PT32X003F4X6C*	16	2	480	M0	48	2.2-3.6	17+1	1	2	1	1	2	1	1	-	48	32	12ext + 2int	-	√	-40~85	14, 15	QFN20(3*3)	0.55mm	0.4mm	Tray
PT32X003F4X6CR*	16	2	480	M0	48	2.2-3.6	17+1	1	2	1	1	2	1	1	-	48	32	12ext + 2int	-	√	-40~85	14, 15	QFN20(3*3)	0.55mm	0.4mm	Tape & Reel

\* Under Development